

Physics 559 -- Advanced Physics Lab  
SUNY College at Cortland  
Physics Department

Catalog Description: The development of additional techniques and skills in a special area of physics. May be repeated to perform different experiments. Prerequisites: PHY 202 or consent of department. (3 cr. hr.)

Required Text: *Individual Learning Program in Digital Techniques* (Heath Company, Benton Harbor, Michigan, 1975)

Course Information:  
Fall 2008 Term, 3 credit hours  
Meeting Times TBA  
Bowers 139

Professor Information:  
Dr. Brice Smith  
Bowers 143 (office)  
Bowers 141 (lab)  
brice.smith@cortland.edu

Office Hours: MWF 9:00-10:00am and 12:30-2:00pm  
(Please make an appointment if you would like to see me at another time.)

Evaluation of Student Grades will be based on the total points accumulated from the following components:

2 One Hour Exams	50 points each
Final Exam	100 points
Lab Assignments	350 points

Maximum Points Possible: 550

Problem sets will typically be handed out on Wednesday and will be due the following Wednesday when an answer key will be provided. Late problem sets will not generally be accepted. A final curve will be established at the end of the course for the assignment of individual letter grades.

Tests: Tests will be given on Oct. 14 and Dec. 1

Students with a Disability:

If you are a student with a disability and wish to request accommodations, please contact the Office of Student Disability Services located in B-40 VanHoesen or call (607) 753-2066 for an appointment. Information regarding your disability will be treated in a confidential manner. Because many accommodations require early planning, requests for accommodations should be made as early as possible.

PHYSICS 559 -- FALL 2008

<u>Week Of</u>	<u>Topic</u>	<u>Text</u>
Aug. 25	Introduction to Digital Techniques <i>(No Lab)</i>	Unit 1
Sept. 1	Semiconductor Devices and Circuits <i>Experiment 1</i>	Unit 2
Sept. 8	Digital Logic Circuits <i>Experiments 2, 3, 4</i>	Unit 3
Sept. 15, 22	Digital Integrated Circuits <i>Experiments 5 and 6</i>	Unit 4
Sept. 29	Boolean Algebra <i>Experiments 7 and 8</i>	Unit 5
Oct. 6	Flip-Flops and Registers <i>Experiments 9, 10, and 11</i>	Unit 6
Oct. 13, 20, 27	Sequential Logic Circuits FIRST TEST <i>Experiments 11, 12, 13, 14, 15, 16, and 17</i>	Unit 7
Nov. 3, 10	Combinational Logic Circuits <i>Experiments 18, 19, 20, 21, and 22</i>	Unit 8
Nov. 17, 24	Digital Design <i>Experiments 23 and 24</i>	Unit 9
Dec. 1	Review and Catch-up Week SECOND TEST <i>(No Lab)</i>	